

**ABSTRACT OF THE DISCLOSURE**

**[0039]** A differential register slave structure is presented. In one embodiment, a differential register includes a storage node (218, 318). The storage node (218, 318) stores and holds the differential values generated by the differential register. In one embodiment of the present invention, on power-up, when the state of various clocks (i.e., master, slave) in the differential register may be indeterminate, the storage node (218, 318) will discharge the differential values and the differential register will produce a differential output.